



RENESAS TRAINEE PROGRAM 2017

I. Opening position

- 15 Software Design Trainees (Embedded)
- 12 Hardware Design Trainees (Logic/Physical/DFT)

II. Working condition

- Working place: Renesas office, Tan Thuan Road, District 7, Ho Chi Minh City
- Working hour: at least 2.5 working days per week from Mondays to Fridays, from 8am to 5pm
- Training allowance
- 24/7 accident insurance
- Free lunch every working day
- Join real projects of cutting edge technologies in IC design and embedded system
- Opportunities to become permanent engineers through direct entry scheme

III. Requirement

- University students in 3rd year and above with major in Electronics, Telecoms, Automation, Mechatronics, Computer Science, Computer Engineering, Information Technology, Physics-Electronics, Physics-Computer Science, Maths-Computer Science
- Good at technical skill and English skill, strong passion in IC Design and Embedded System
- Can work at least 2.5 working days per week from Mondays to Fridays from 8am to 5pm

IV. Recruitment schedule

- Written test and interview: from November 2016 to February 2017
- Start working date: March 2017
- Register at <https://goo.gl/forms/Eh4ABEB8hMVS6gyD2> or jobs@rvc.renesas.com
- Contact us at jobs@rvc.renesas.com or www.facebook.com/RenesasDesignVietnam