

## RECRUITMENT NOTICE

### About Uniquify

Headquartered in Silicon Valley, Uniquify ([www.uniquify.com](http://www.uniquify.com)) is a privately held ASIC design services and semiconductor IP solutions provider. The company has design centers in Santa Clara, California and Pune, India; and it maintains sales offices in Austin, Santa Clara, Seoul, Taipei and Tel Aviv. Uniquify provides leading edge SoC design and IP solutions. We offer a wide range of “ideas2silicon” services that span design specification, RTL, logic design/verification, physical implementation, and manufacturing operations. Perseus, our proprietary design management system allows us to deliver consistent design closure and reduced schedules on even the most complex SoC designs.

### Opportunities:

To join Uniquify Vietnam (UNQVN), New Grads will have more chances to attend technical training courses, relating to UNQVN’s key job fields:

1. Front-to-back SOC design in leading edge technology such as 28nm and beyond
2. Complex SOC design integration and verification.
3. Comprehensive Physical Design from RTL to GDS with in-house Perseus™ Design Management System
4. Design-For-Test and Multiple Corners Static Timing Analysis to enhance the yield and reliability
5. High speed analog design for the DDR sub-system, HDMI interface, LVDS, PLL, USB, SATA IO
6. Ultra low power and high density mix-signal analog IP
7. Leading-edge custom layout to address the process variation or advance structure in FinFet or FDSOI technology.
8. Design Automation (CAD) to enable the consistency and predictable tape out schedule and risk mitigation for complex SOC chip.

### Requirements:

- In the last year of undergraduate program in Electronics Engineering or Physics Engineering.
- Strong analytical, logical thinking, debugging skills.
- Exposure to a scripting languages such as TCL, Perl, and/or C-shell.
- Prefer to working in Linux environment.
- Good understanding of Fundamentals of Synchronous logic design.
- Coursework in digital IC design.

- Experience with Verilog coding and verification.
- Project or industry experience in ASIC (Backend design: DFT, Layout, STA).
- How to use circuit simulation tools (HSPICE or Spectre)
- Understand process window & variation in semiconductor design
- Understand electronics fundamentals: passive devices (R,L,C), active devices (BJT, MOSFET), Laplace transform, transfer function
- Good communication skills, positive attitude, and ability to work in a team environment.
- High GPA score
- A good command of English.

### How to apply:

Please send a package of your documents to email address [unq\\_vn\\_hr@uniquify.com](mailto:unq_vn_hr@uniquify.com) for screening:

- Resume in English
- Transcript
- TOEIC/ IELTS/ TOEFL certificate/ result.